

35927 U.S. PTO
10/025665



JK 1109

U.S. UTILITY Patent Application

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PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10025665	12/19/2001	364		2835	B Lee

****APPLICANTS:** Stoddard Donald; Schumacher Matthew;

****CONTINUING DATA VERIFIED:** *None*

**** FOREIGN APPLICATIONS VERIFIED:** *None*

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	MAX.0002US
Verified and Acknowledged Examiners's initials <i>JS</i>		
TITLE : Method and apparatus for adding inductance to printed circuits		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL		Applicati n Examiner	
DISCLAIMER		PREPARED FOR ISSUE	
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